

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Sau Ching Wong
Assignee: Multi Level Memory Technology
Title: Efficient Redundancy System For Flash Memories With Uniformly
Sized Blocks
Serial No.: Not assigned yet Filing Date: June 30, 2003
Examiner: Hur, Jung H. Group Art Unit: 2824
Docket No.: MLM003US1D

San Jose, California
June 30, 2003

COMMISSIONER FOR PATENTS
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INFORMATION DISCLOSURE STATEMENT UNDER CFR § 1.97(b)

Dear Sir:

Pursuant to 37 C.F.R. § 1.56, § 1.97 and § 1.98, Applicants call to the attention of the Examiner the documents listed on the attached substitute PTO Form-1449A.

Copies of the listed documents, other than those documents marked with an asterisk adjacent the cite no. in the substitute PTO Form-1449A, are enclosed. The documents marked with an asterisk were cited or submitted in U.S. Patent Application No. 09/927,693, filed on August 9, 2001, which the present application relies on for an earlier effective filing date under 35 U.S.C. 120.

Citation of the above documents shall not be construed as:

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OF
DAVID MILLERS
6560 ASHFIELD COURT
SAN JOSE, CA 95120
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FAX (408) 927-6701

No fee is believed required for submission of this information disclosure statement. However, the Commissioner is hereby authorized to charge any fees that may be required for consideration of this information disclosure statement to Deposit Account No. 50-1767.

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Respectfully submitted,



David Millers

Attorney for Applicants

Reg. No. 37,396

THE PATENT LAW OFFICES
OF
DAVID MILLERS

6560 ASHFIELD COURT
SAN JOSE, CA 95120
(408) 927-6700
FAX (408) 927-6701

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Substitute for form 1449A/PTO <h2 style="text-align: center; margin: 10px 0;">INFORMATION DISCLOSURE STATEMENT BY APPLICANT</h2> <p style="text-align: center; margin-top: 10px;">(use as many sheets as necessary)</p>			<p style="text-align: center; margin: 0;">C mplete if Known</p> <table border="1" style="width: 100%; border-collapse: collapse;"> <tr> <td style="width: 60%;">Application Number</td> <td></td> </tr> <tr> <td>Filing Date</td> <td>June 30, 2003</td> </tr> <tr> <td>First Named Inventor</td> <td>Sau Ching Wong</td> </tr> <tr> <td>Group Art Unit</td> <td>2824</td> </tr> <tr> <td>Examiner Name</td> <td>Hur, J.</td> </tr> <tr> <td>Attorney Docket Number</td> <td>MLM003US1D</td> </tr> </table>		Application Number		Filing Date	June 30, 2003	First Named Inventor	Sau Ching Wong	Group Art Unit	2824	Examiner Name	Hur, J.	Attorney Docket Number	MLM003US1D
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OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
	*6	Intel, "Preliminary Datasheet for 1.8 Volt Intel® Wireless Flash Memory (W18)," Order No. 290701-002 (January 2001) Pages i-79.	
	*7	Pathak, B. et al., 2001 IEEE International Solid-State Circuits Conference, Session 2, Non-volatile Memories, "2.3 A 1.8V 64Mb 100MHz Flexible Read-While Write Flash Memory" (Feb. 5, 2001).	

Examiner Signature		Date Considered	
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* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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